Attorney Docket No.: 0180187

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of: **Wang, et al.**

Serial No.: 10/678,446

Filed: October 3, 2003

For: **Efficient and Accurate Sensing Circuit and Technique for Low Voltage Flash Memory Devices**

Art Unit: 2818

Examiner: Mai, Son Luu

RESPONSE TO FINAL OFFICE ACTION

Mail Stop AF
Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir/Madam:

This is in response to the ***Final Office Action*** dated November 22, 2004 in the above-referenced patent application. Please enter and consider the following remarks.

PAGE 6/12 * RCVD AT 1/3/2005 1:19:49 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/4 * DNIS:8729308 * CSID:949 282 1002 * DURATION (mm-ss):04-38